

JUL 19 2004

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Date: July 15, 2004

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From: Christopher S. Daly, Esq.

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MESSAGE

Re: U.S. Patent Application of Rafael Reif et al.

Application No.: 10/655,854

Entitled: MULTI-LAYER INTEGRATED SEMICONDUCTOR STRUCTURE

Filed On: September 5, 2003

DCM Case No.: MIT-136AUS

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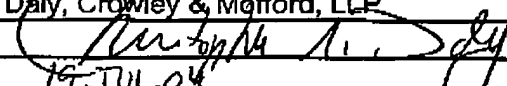
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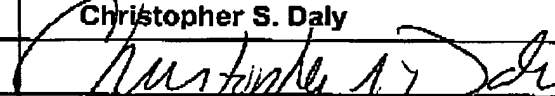
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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/655,854	
	Filing Date	September 5, 2003	
	First Named Inventor	Rafael Reif	
	Art Unit	2811	
	Examiner Name	Not Yet Assigned	
Total Number of Pages in This Submission	42	Attorney Docket Number	MIT-136AUS

ENCLOSURES (Check all that apply)		
<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment/Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Assignment Papers (for an Application) <input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____	<input type="checkbox"/> After Allowance communication to Technology Center (TC) <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): Return Postcard PTO SB/08A with Eight (8) References Cited
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Firm or Individual name	Christopher S. Daly Daly, Crowley & Mofford, LLP
Signature	
Date	19 JUL 04

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Appl. No. : 10/655,854
Applicant : Rafael Reif et al.
Filed : September 5, 2003
T.C./A.U. : 2811
Examiner : Not Yet Assigned

Confirmation No.: 7254

JUL 19 2004

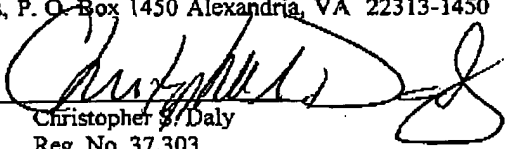
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By:


Christopher J. Daly
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Mail Stop Amendment
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Dear Sir:

It is desired to cite for the record in this application the enclosed documents listed on the attached copy of PTO form PTO/SB/08A (Formerly #1449). The paragraph(s) marked below are applicable to this Information Disclosure Statement.

[X] (1) The enclosed Information Disclosure Statement is being filed: within three months of the filing date; or within three months of the entry of the national stage of the above-identified application; or before the mailing of a first Office Action on the merits; or before the mailing of a first Office Action after the filing of a request for continued examination under 37 C.F.R. §1.114. Accordingly, Applicant(s) believes that no fee or statement is required.

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Docket No. MIT-136AUS

[] (2) Pursuant to 37 C.F.R. § 1.97(c), the enclosed Information Disclosure Statement is being filed before the mailing date of a final action or a notice of allowance and is accompanied by the fee set forth in 37 C.F.R. § 1.17(p).

SUBMISSION UNDER 37 C.F.R. § 1.97(c)

INCLUDING A STATEMENT UNDER 37 C.F.R. § 1.97(e)(1)

[] (3) Pursuant to 37 C.F.R. § 1.97(c), the enclosed Information Disclosure Statement is being filed before the mailing date of a final action or a notice of allowance and is accompanied by a statement under 37 C.F.R. § 1.97(e)(1). The undersigned hereby states that each item of information contained in the attached Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application mailed not more than three months prior to the filing of the accompanying Information Disclosure Statement.

SUBMISSION UNDER 37 C.F.R. § 1.97(c)

INCLUDING A STATEMENT UNDER 37 C.F.R. § 1.97(e)(2)

[] (4) Pursuant to 37 C.F.R. § 1.97(c), the enclosed Information Disclosure Statement is being filed before the mailing date of a final action or a notice of allowance and is accompanied by a statement under 37 C.F.R. § 1.97(e)(2). The undersigned hereby states that no item of information contained in the accompanying Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned, after making reasonable inquiry, no item of information contained in the accompanying Information Disclosure Statement was known to any individual having a duty of disclosure as set forth in 37 C.F.R. § 1.56 (c) more than three months prior to the filing of the accompanying Information Disclosure Statement.

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SUBMISSION UNDER 37 C.F.R. § 1.97(c)INCLUDING STATEMENTS UNDER 37 C.F.R. §§ 1.704(d) AND 1.97(e)(1)

[] (5) Pursuant to 37 C.F.R. § 1.97(c), the enclosed Information Disclosure Statement is being filed before the mailing date of a final action or a notice of allowance and is accompanied by statements under 37 C.F.R. §§ 1.704(d) and 1.97(e)(1). The undersigned hereby states that each item of information contained in the attached Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application mailed not more than three months prior to the filing of the accompanying Information Disclosure Statement and was not received by any individual designated in §1.56(c) more than thirty days prior to the filing of the accompanying Information Disclosure Statement.

SUBMISSION UNDER 37 C.F.R. § 1.97(d)INCLUDING A STATEMENT UNDER 37 C.F.R. §1.97(e)(1)

[] (6) Pursuant to 37 C.F.R. § 1.97(d), the enclosed Information Disclosure Statement is being filed on or before payment of the issue fee and is accompanied by a statement under 37 C.F.R. § 1.97(e)(1) and the fee required under 37 C.F.R. § 1.17(p). The undersigned hereby states that each item of information contained in the accompanying Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement.

SUBMISSION UNDER 37 C.F.R. § 1.97(d)INCLUDING A STATEMENT UNDER 37 C.F.R. §1.97(e)(2)

[] (7) Pursuant to 37 C.F.R. § 1.97(d), the enclosed Information Disclosure Statement is being filed on or before payment of the issue fee and is accompanied by a statement under 37 C.F.R. § 1.97(e)(2) and the fee required under 37 C.F.R. § 1.17(p). The undersigned hereby states that no item of information contained in the accompanying Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned, after making reasonable inquiry, no item of information contained in the accompanying Information Disclosure Statement was known to

Appl. No. 10/655,854

Docket No. MIT-136AUS

any individual having a duty of disclosure as set forth in 37 C.F.R. § 1.56(c) more than three months prior to the filing of the accompanying Information Disclosure Statement.

SUBMISSION UNDER 37 C.F.R. § 1.97(d)

INCLUDING STATEMENTS UNDER 37 C.F.R. §§ 1.704(d) AND 1.97(e)(1)

[] (8) Pursuant to 37 C.F.R. § 1.97(d), the enclosed Information Disclosure Statement is being filed on or before payment of the issue fee and is accompanied by statements under 37 C.F.R. §§ 1.704(d) and 1.97(e)(1) and the fee required under 37 C.F.R. § 1.17(p). The undersigned hereby states that each item of information contained in the attached Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application mailed not more than three months prior to the filing of the accompanying Information Disclosure Statement and was not received by any individual designated in § 1.56(c) more than thirty days prior to the filing of the accompanying Information Disclosure Statement.

The filing of this Information Disclosure Statement is not a representation by the undersigned as to personal knowledge of the contents of every word or phrase of the material enclosed or that reliance on other suitably trained professionals has not been made.

If a search report of a searching agency is enclosed identifying the nature of the relevance of each document, such a designation is deemed to satisfy Rule 98(a) (3) even if in a foreign language, since the few terms of relevance therein are deemed of universal cognizance. However, Applicant(s) does not necessarily adopt the position reflected by that report.

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The Commissioner is hereby authorized to charge payment of any additional fees associated with this communication or credit any overpayment to Deposit Account No. 500845.

Dated: 19 JUL 04

Respectfully submitted,

DALY, CROWLEY & MOFFORD, LLP

By: 

Christopher S. Daly

Reg. No. 37,303

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		Complete if Known	
		Application Number	10/655,854
		Filing Date	September 5, 2003
		First Named Inventor	Rafael Reif
		Art Unit	2811
		Examiner Name	Not Yet Assigned
Sheet 1 of 1	Attorney Docket Number	MIT-136AUS	

OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		OSAMU TABATA, Anisotropic Etching of Silicon in TMAH Solutions, Toyota Central Research and Development Laboratories, Inc., February 21, 1992, Pages 51-57, Japan	
		PHILIP M. SAILER, Creating 3D Circuits Using Transferred Films, Circuits & Devices, November 1997, Pages 27-30	
		TAKUJI MATSUMOTO, New Three-Dimensional Wafer Bonding Technology Using the Adhesive Injection Method, March, 1998, Pages 1217-1221, Jpn. J. Appl. Phys. Vol. 37 (1998)	
		A. FAN, Copper Wafer Bonding, Electrochemical and Solid-State Letters, 1999, Pages 534-536, Cambridge, Massachusetts	
		ARIFUR RAHMAN, Wire-Length Distribution of Three-Dimensional Integrated Circuits, 1999, Pages 233-235	
		VICTOR W.C. CHAN, Three Dimensional CMOS Integrated Circuits on Large Grain Polysilicon Films, EEE, Hong Kong University of Science and Technology, 2000, Pages 00-161 - 00-164 Hong Kong	
		K.W. LEE, Three-Dimensional Shared Memory Fabricated Using Wafer Stacking Technology, Dept. of Machine Intelligence and Systems Engineering, 2000, Pages 00-165 - 00-168, Hong Kong	
		KUAN-NENG CHEN, Microstructure Examination of Copper Wafer Bonding, Microsystem Technology Laboratories, December 20, 2000, Pages 331-335, Cambridge, Massachusetts	

Examiner Signature	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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